

*Alt Comp*

surface of the substrate to define the RF ports 104, 106, and the reference transmission line path 108 and phase shift transmission line path 110. The MEM switch 50A is connected by wire bond connections 112, 114 between the port 104 and one end of the reference path 108. Elements of the switch 50A are diagrammatically shown in FIG. 4, including the RF ports indicated as 50A-1 and 50A-2 to which the wire bond connections are made. The cantilever beam is shown as element 50A-3. The DC bias connections are made at 50A-4 and 50A-5. The other end of the reference path 108 is connected though switch 50B to the RF port 106.--

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Amend the paragraph bridging pages 10-11 to the following:

--FIG. 6B shows a "4.5" bit phase shifter 150 using SP3T switch circuits. This circuit has three sections 152, 154, 156, instead of two sections as in the circuit 140. Each section has two SP3T MEM switches to select a reference path, a first phase shift path or a second phase shift path. The sections are connected in series.--

IN THE CLAIMS

*Cancel*

Cancel Claims 1-4, 6-8, 10-12, 15-20 and 22-24 without prejudice or disclaimer of the subject matter contained therein.

*Amend*

5. (Amended) The array of Claim 25 wherein said MEM switches are single-pole-single-throw (SPST) switches including an armature for opening and closing the RF signal